

AMENDMENT

IN THE SPECIFICATION:

Please amend Page 34, lines 5-13 so as to read as follows:

A. "Fig. 4 is a schematic block diagram of the gate driver 13. The constitution of the gate driver 13 in the present invention is not limited to this. The gate driver 13 of this embodiment has a shift register 41, an output signals from latch circuits (shown representatively in phantom) constituting this shift register 41 are supplied to an output circuit 42. Then, a gate voltage of "H" level or "L" level is applied to a gate line G by the output circuit 42, by which the gate line G is selected."
